

Abstract of the Disclosure

To address the need for a high availability system (100) and
5 method of initializing that address failures that lock up common
communication buses (109) in these systems, the present invention
avoids powering-up peripheral components (e.g., 102-103) that have
previously locked up the bus. It accomplishes this by storing indicators of
successful initializations in memory (105), and then subsequently
10 powering-up components only if such an indicator was stored for that
component's last power-up.

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